

PATENT

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*Stephanie Daryaie*  
Stephanie Daryaie

Applicant : Janardhanan S. Ajit  
Application No. : 10/621,005  
Filed : July 16, 2003  
Title : SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT  
(I/O) CIRCUIT  
  
Grp./Div. : 2816  
Examiner : Tuan Thieu Lam  
  
Docket No. : 50716/DBP/B600

## TRANSMITTAL OF DRAWINGS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Pasadena, CA 91109-7068  
January 7, 2004

Commissioner:

Enclosed is 1 sheet of drawing for this application. As discussed with the Examiner, it is being submitted as corrected drawing to replace the corresponding formal drawing as originally filed. Also enclosed is an "Annotated Marked-up Drawing" showing the changes made.

Respectfully submitted,

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